

RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

SDT3005 provides designers with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness. DFN2*2-6J package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

FEATURES

- TrenchFET power MOSFET
- Low $R_{DS(on)}$
- Typical ESD protection

APPLICATIONS

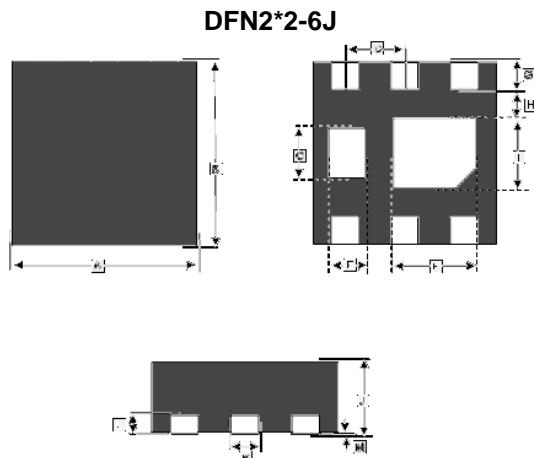
- Load switch and battery protection

MARKING

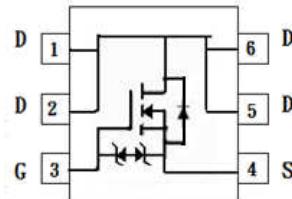
3005

PACKAGE INFORMATION

Package	MPQ	Leader Size
DFN2*2-6J	3K	7 inch



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	1.924	2.076	H	0.20	-
B	1.924	2.076	I	0.85	1.05
C	0.46	0.66	J	0.70	0.90
D	0.65	TYP.	K	0.20	0.40
E	0.20	0.40	L	0.203	REF
F	0.80	1.00	M	0.00	0.05
G	0.174	0.326			



ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±10	V
Continuous Drain Current	I _D	5	A
Pulsed Drain Current ¹	I _{DM}	20	A
Thermal Resistance from Junction to Ambient	R _{θJA}	250	°C / W
Operating Junction and Storage Temperature	T _J , T _{STG}	150, -55~150	°C

ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

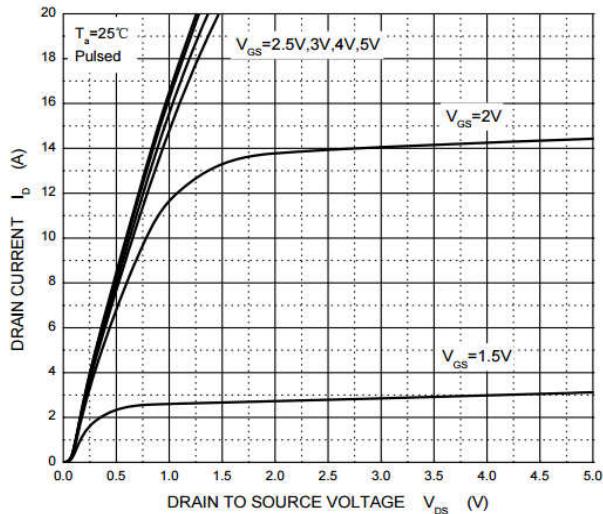
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	30	-	-	V	$\text{V}_{GS}=0$, $I_D=250\mu\text{A}$
Gate-Source Leakage Current	I_{GSS}	-	-	± 10	μA	$\text{V}_{GS} = \pm 10\text{V}$, $\text{V}_{DS}=0$
Drain-Source Leakage Current	I_{DSS}	-	-	1	μA	$\text{V}_{DS}=30\text{V}$, $\text{V}_{GS}=0$
Gate-Threshold Voltage ²	$\text{V}_{GS(\text{th})}$	0.6	-	1	V	$\text{V}_{DS}=\text{V}_{GS}$, $I_D=250\mu\text{A}$
Forward Transconductance ²	g_{fs}	-	15	-	S	$\text{V}_{DS}=5\text{V}$, $I_D=4\text{A}$
Diode Forward Voltage ²	V_{SD}	-	-	1	V	$I_S=1\text{A}$, $\text{V}_{GS}=0$
Static Drain-Source On-Resistance ²	$R_{DS(\text{ON})}$	-	-	42	mΩ	$\text{V}_{DS}=10\text{V}$, $I_D=5\text{A}$
		-	-	44		$\text{V}_{DS}=4.5\text{V}$, $I_D=5\text{A}$
		-	-	50		$\text{V}_{GS}=2.5\text{V}$, $I_D=4\text{A}$
Dynamic Characteristics						
Input Capacitance	C_{iss}	-	245	-	pF	$\text{V}_{DS}=15\text{V}$ $\text{V}_{GS}=0\text{V}$ $f=1\text{MHz}$
Output Capacitance	C_{oss}	-	35	-		
Reverse Transfer Capacitance	C_{rss}	-	20	-		
Switching Characteristics						
Total Gate Charge	Q_g	-	10	-	nC	$I_D=4\text{A}$ $\text{V}_{DS}=15\text{V}$ $\text{V}_{GS}=10\text{V}$
Gate-Source Charge	Q_{gs}	-	0.5	-		
Gate-Drain Charge	Q_{gd}	-	1	-		
Turn-On Delay Time	$T_{d(on)}$	-	2	-	nS	$\text{V}_{DD}=15\text{V}$ $\text{V}_{GS}=10\text{V}$ $R_L=3.75\Omega$ $R_{GEN}=3\Omega$
Rise Time	T_r	-	3.5	-		
Turn-Off Delay Time	$T_{d(off)}$	-	22	-		
Fall Time	T_f	-	3.5	-		

Notes:

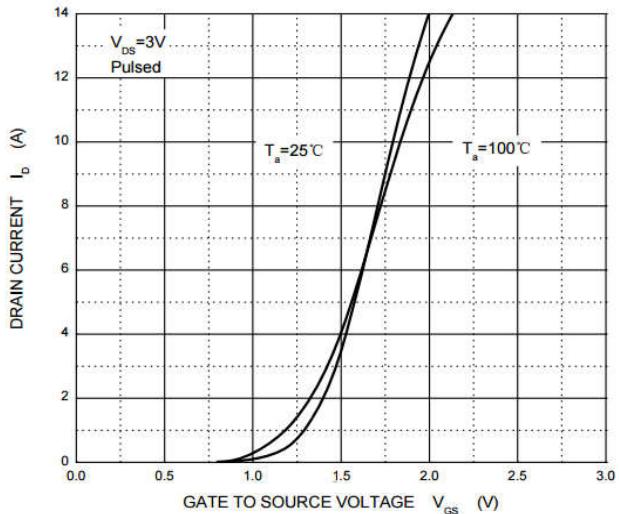
1. Repetitive rating : Pulse width limited by junction temperature.
2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

CHARACTERISTIC CURVES

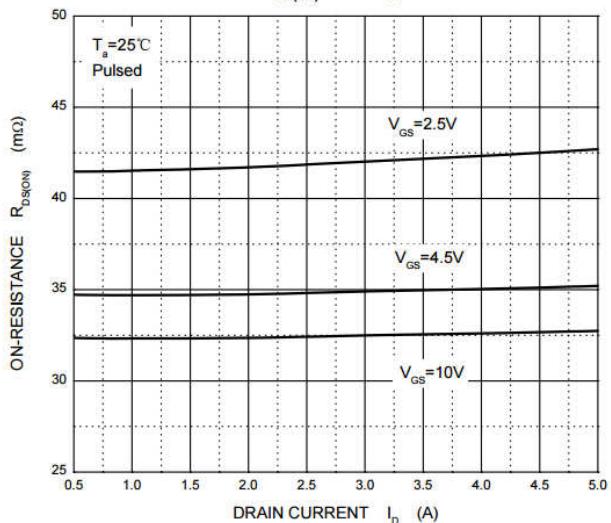
Output Characteristics



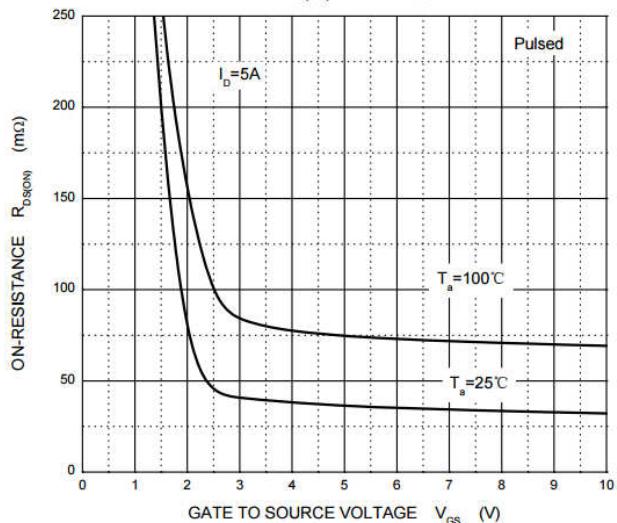
Transfer Characteristics



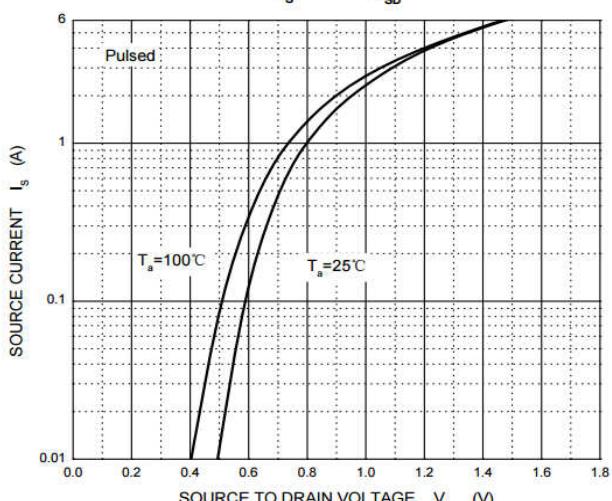
$R_{DS(ON)}$ — I_D



$R_{DS(ON)}$ — V_{GS}



I_S — V_{SD}



Threshold Voltage

