

RoHS Compliant Product
 A Suffix of “-C” specifies halogen & lead-free

DESCRIPTIONS

The SMS2002-C is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The SMS2002-C meet the RoHS and Green Product requirement with full function reliability approved.

FEATURES

- Reliable and Rugged
- Green Device Available
- ESD Protection

MARKING

2002

PACKAGE INFORMATION

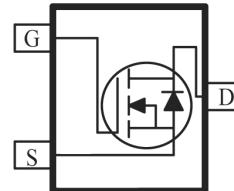
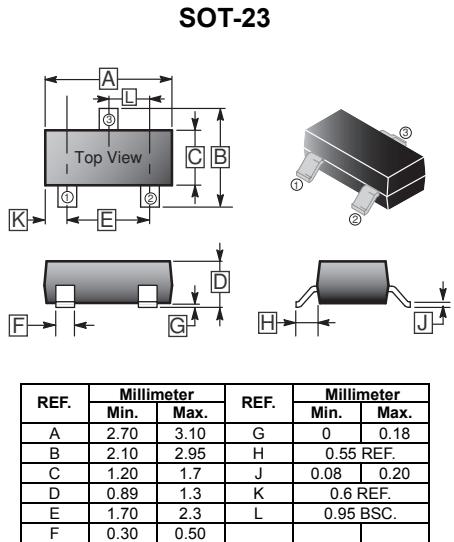
Package	MPQ	Leader Size
SOT-23	3K	7 inch

ORDER INFORMATION

Part Number	Type
SMS2002-C	Lead (Pb)-free and Halogen-free

MAXIMUM RATINGS

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±12	V
Continuous Drain Current, @V _{GS} =4.5V ¹	I _D	2.2	A
T _A =70°C	I _D	1.8	
Pulsed Drain Current ³	I _{DM}	8	A
Power Dissipation	T _A =25°C	P _D	W
Operating Junction & Storage Temperature Range	T _J , T _{STG}	-55~150	°C
Thermal Resistance Rating			
Thermal Resistance Junction-ambient ¹	R _{θJA}	t≤5Sec , 125	°C / W
		Steady state , 250	
Thermal Resistance Junction-ambient ²	R _{θJA}	415	



ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	20	-	-	V	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$
Gate-Threshold Voltage	$V_{GS(\text{TH})}$	0.45	-	1	V	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$
Forward Transconductance	g_{fs}	-	10	-	S	$V_{DS}=5\text{V}$, $I_D=2\text{A}$
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{DS}=0\text{V}$, $V_{GS}=\pm 12\text{V}$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$V_{DS}=16\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$
		-	-	5		$V_{DS}=16\text{V}$, $V_{GS}=0\text{V}$, $T_J=55^\circ\text{C}$
Drain-Source On Resistance ⁴	$R_{DS(\text{ON})}$	-	-	100	$\text{m}\Omega$	$V_{GS}=4.5\text{V}$, $I_D=2\text{A}$
		-	-	140		$V_{GS}=2.5\text{V}$, $I_D=1\text{A}$
Total Gate Charge	Q_g	-	4.7	-	nC	$V_{DS}=15\text{V}$
Gate-to-Source Charge	Q_{gs}	-	0.68	-		$V_{GS}=4.5\text{V}$
Gate-to-Drain Charge	Q_{gs}	-	1.3	-		$I_D=2\text{A}$
Turn-on Delay Time	$T_{d(\text{on})}$	-	1.4	-	nS	$V_{DS}=10\text{V}$
Rise Time	T_r	-	40	-		$I_D=2\text{A}$
Turn-off Delay Time	$T_{d(\text{off})}$	-	12.4	-		$V_{GS}=4.5\text{V}$
Fall Time	T_f	-	5.6	-		$R_G=3.3\Omega$
Input Capacitance	C_{iss}	-	220	-	pF	$V_{DS}=15\text{V}$
Output Capacitance	C_{oss}	-	38	-		$V_{GS}=0\text{V}$
Reverse Transfer Capacitance	C_{rss}	-	32	-		f=1MHz
Source-Drain Diode						
Continuous Source Current ¹	I_s	-	-	2.2	A	
Pulsed Source Current ³	I_{SM}	-	-	8	A	
Diode Forward Voltage ⁴	V_{SD}	-	-	1.2	V	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$
Reverse Recovery Time	t_{rr}	-	8.9	-	nS	$I_F=2\text{A}, dI/dt=100\text{A}/\mu\text{s}$,
Reverse Recovery Charge	Q_{rr}	-	1.7	-	nC	$T_J=25^\circ\text{C}$

Notes:

1. Surface Mounted on 1" x 1" FR4 Board with 2OZ copper.
2. When mounted on Min. copper pad.
3. Pulse width limited by maximum junction temperature, Pulse Width $\leq 10\mu\text{s}$, Duty Cycle $\leq 1\%$.
4. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

CHARACTERISTIC CURVES

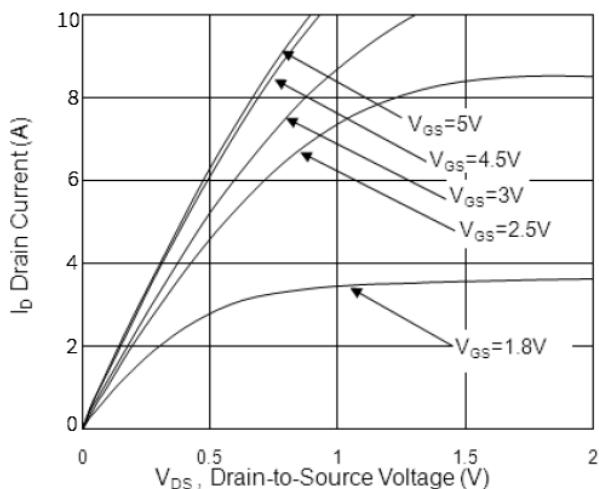


Fig.1 Typical Output Characteristics

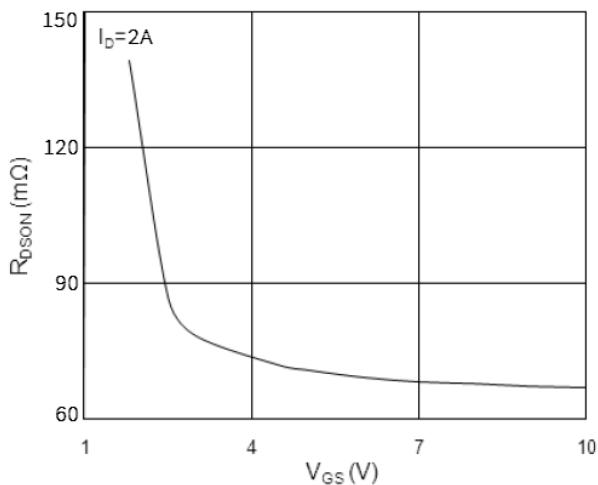


Fig.2 On-Resistance vs. Gate-Source

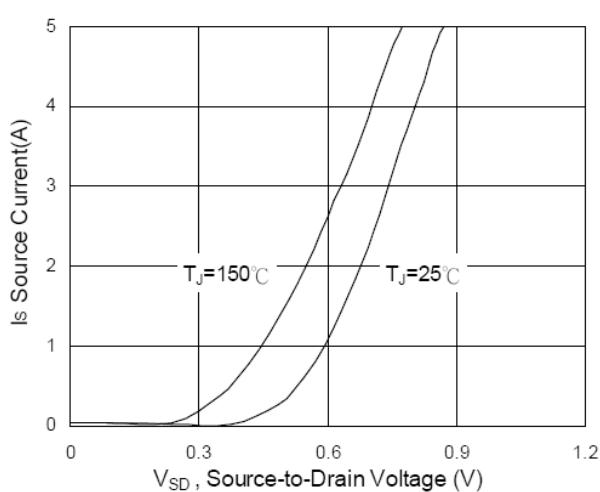


Fig.3 Forward Characteristics Of Reverse

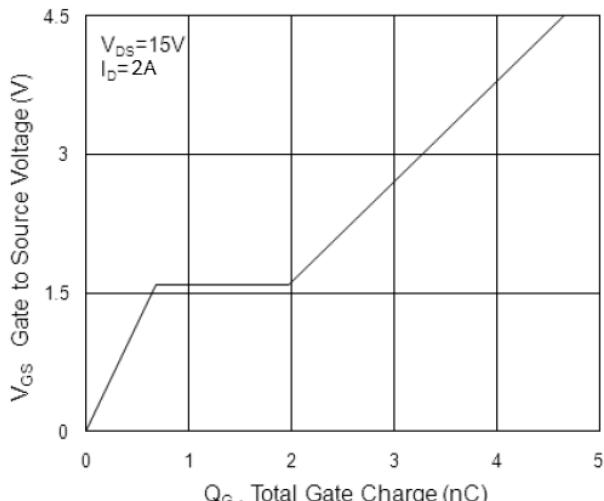


Fig.4 Gate-Charge Characteristics

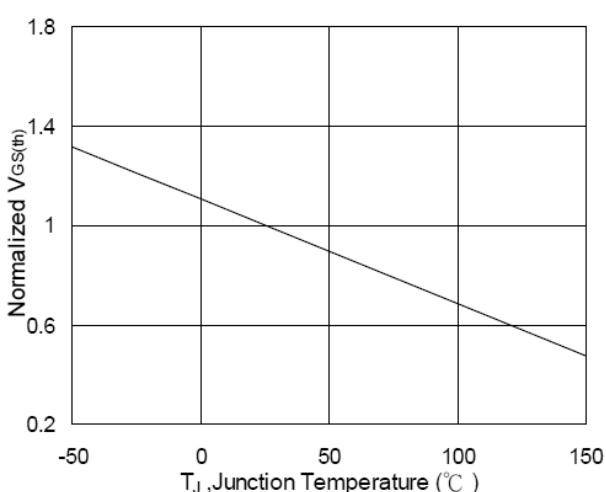


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

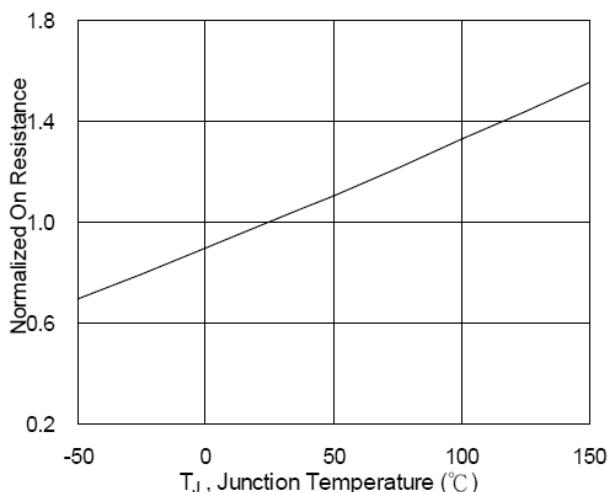


Fig.6 Normalized $R_{DS(ON)}$ vs. T_J

CHARACTERISTIC CURVES

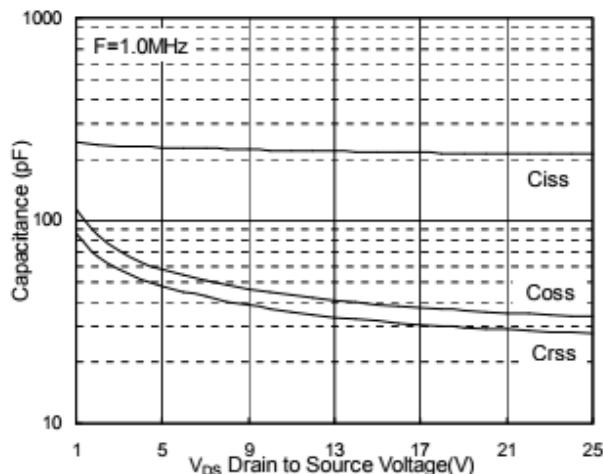


Fig.7 Capacitance

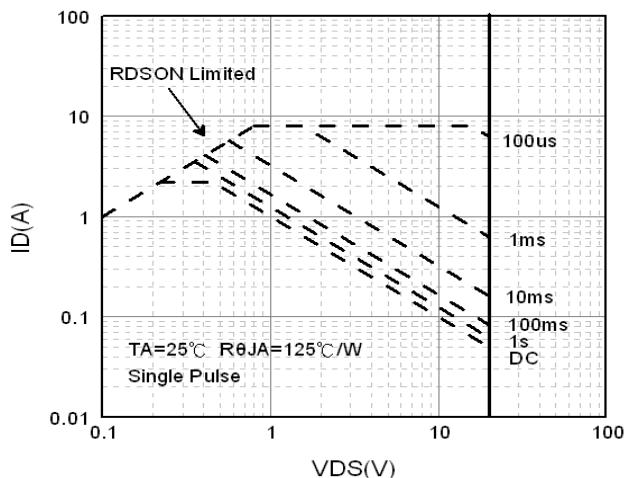


Fig.8 Safe Operating Area

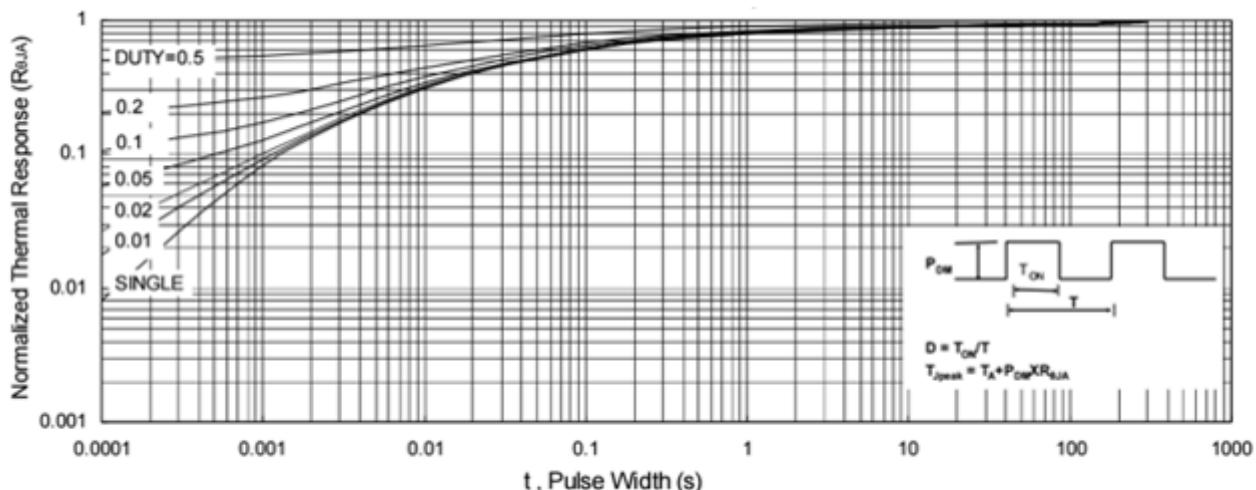


Fig.9 Normalized Maximum Transient Thermal Impedance

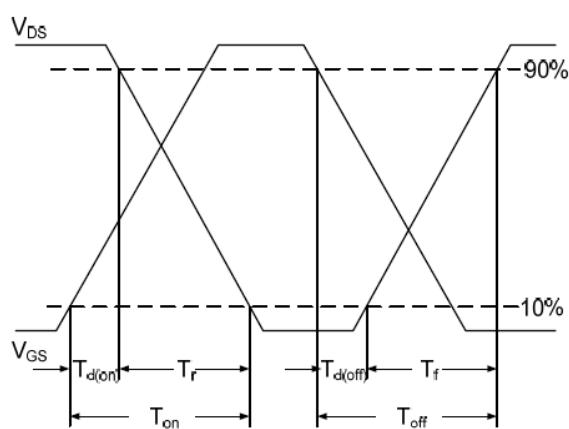


Fig.10 Switching Time Waveform

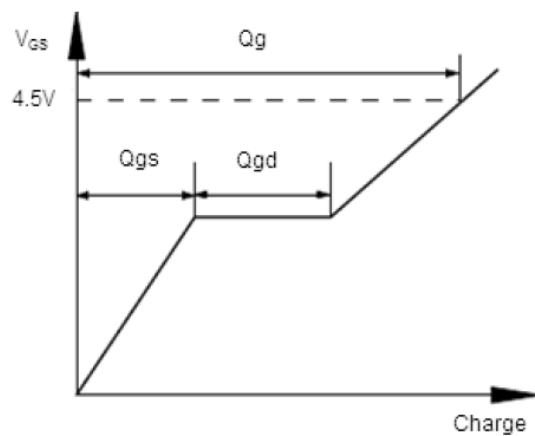


Fig.11 Gate Charge Waveform