

RoHS Compliant Product
 A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

The SMS3001-C is the highest performance trench P-ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the small power switching and load switch applications.

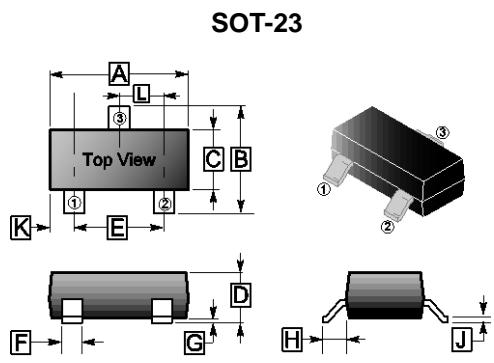
The SMS3001-C meet the RoHS and Green Product Requirement with full function reliability approved.

FEATURES

- Advanced High Cell Density Trench Technology
- Super Low Gate Charge
- Green Device Available

MARKING

3001



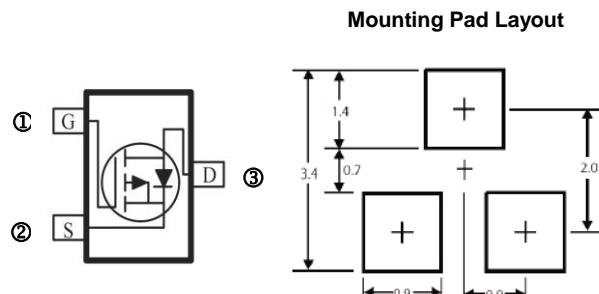
REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.65	3.10	G	0	0.18
B	2.10	3.00	H	0.55	REF.
C	1.10	1.80	J	0.08	0.26
D	0.89	1.40	K	0.60	REF.
E	1.70	2.30	L	0.95	TYP.
F	0.28	0.55			

PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-23	3K	7 inch

ORDER INFORMATION

Part Number	Type
SMS3001-C	Lead (Pb)-free and Halogen-free



*Dimensions in millimeters

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current @ $V_{GS} = -10V$ ¹	I_D	-4.3	A
		-3.5	
Pulsed Drain Current ³	I_{DM}	-20	A
Power Dissipation	P_D	1	W
Operating Junction & Storage Temperature	T_J, T_{STG}	150, -55~150	°C
Thermal Resistance Ratings			
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	$t \leq 5s, 125$	°C/W
		Steady State, 250	
Thermal Resistance Junction- Ambient ²	$R_{\theta JA}$	313	

ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	-30	-	-	V	$V_{GS}=0$, $I_D = -250\mu\text{A}$
Gate-Threshold Voltage	$V_{GS(\text{th})}$	-0.3	-	-1.2	V	$V_{DS}=V_{GS}$, $I_D = -250\mu\text{A}$
Forward Tranconductance	g_{fs}	-	5.6	-	S	$V_{DS} = -5\text{V}$, $I_D = -3\text{A}$
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{GS} = \pm 12\text{V}$
Drain-Source Leakage Current	I_{DSS}	-	-	-1	μA	$V_{DS} = -24\text{V}$, $V_{GS}=0$
$T_J=55^\circ\text{C}$		-	-	-5		
Static Drain-Source On-Resistance ⁴	$R_{DS(\text{ON})}$	-	-	53	$\text{m}\Omega$	$V_{GS} = -10\text{V}$, $I_D = -4\text{A}$
		-	-	60		$V_{GS} = -4.5\text{V}$, $I_D = -3.5\text{A}$
		-	-	80		$V_{GS} = -2.5\text{V}$, $I_D = -2.5\text{A}$
Total Gate Charge	Q_g	-	11.9	-	nC	$I_D = -3\text{A}$ $V_{DS} = -15\text{V}$ $V_{GS} = -4.5\text{V}$
Gate-Source Charge	Q_{gs}	-	1.8	-		
Gate-Drain Charge	Q_{gd}	-	3	-		
Turn-on Delay Time	$T_{d(\text{on})}$	-	6.6	-	nS	$I_D = -3\text{A}$ $V_{GS} = -4.5\text{V}$ $V_{DD} = -15\text{V}$ $R_G = 3.3\Omega$
Rise Time	T_r	-	27.8	-		
Turn-off Delay Time	$T_{d(\text{off})}$	-	46.2	-		
Fall Time	T_f	-	20.6	-		
Input Capacitance	C_{iss}	-	920	-	pF	$V_{GS}=0$ $V_{DS} = -15\text{V}$ $f = 1\text{MHz}$
Output Capacitance	C_{oss}	-	73	-		
Reverse Transfer Capacitance	C_{rss}	-	71	-		
Source-Drain Diode						
Forward Voltage ⁴	V_{SD}	-	-	-1.2	V	$V_{GS}=0$, $I_S = -1\text{A}$, $T_J=25^\circ\text{C}$
Continuous Source Current ¹	I_S	-	-	-4.3	A	
Pulsed Source Current ³	I_{SM}	-	-	-20	A	

Notes:

1. The data tested by surface mounted on 1inch² FR-4 Board with 2oz copper.
2. When mounted on Min. copper pad.
3. Pulse width limited by maximum junction temperature, Pulse Width $\leq 300\mu\text{s}$, Duty Cycles $\leq 2\%$.
4. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

CHARACTERISTIC CURVES

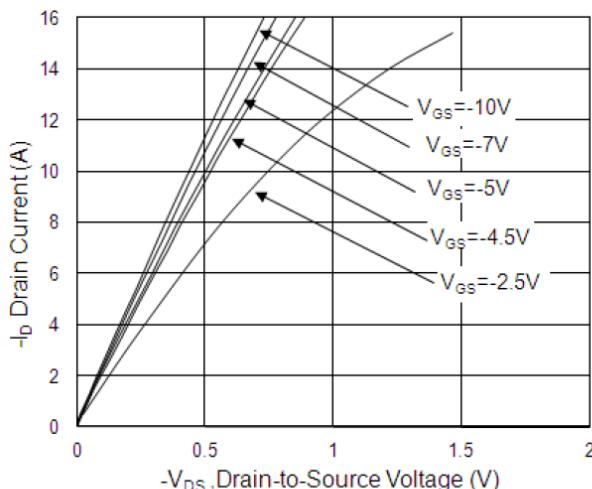


Fig.1 Typical Output Characteristics

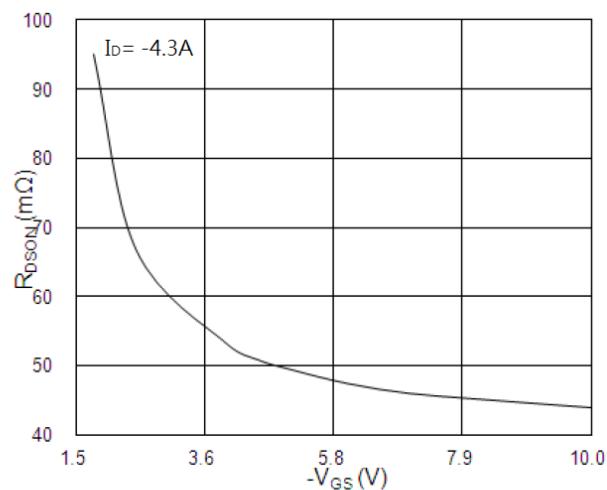


Fig.2 On-Resistance vs. G-S Voltage

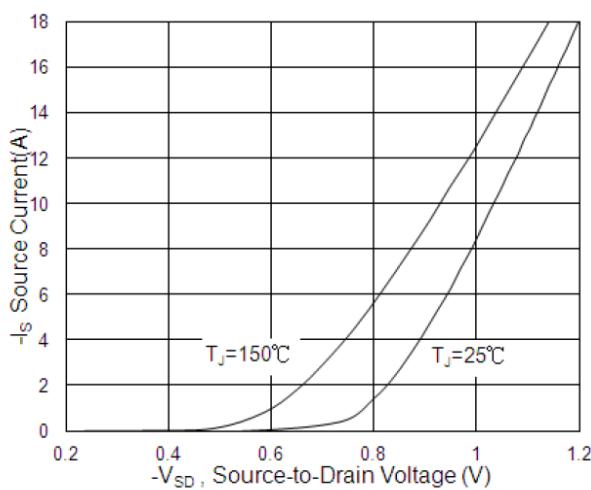


Fig.3 Forward Characteristics Of Reverse

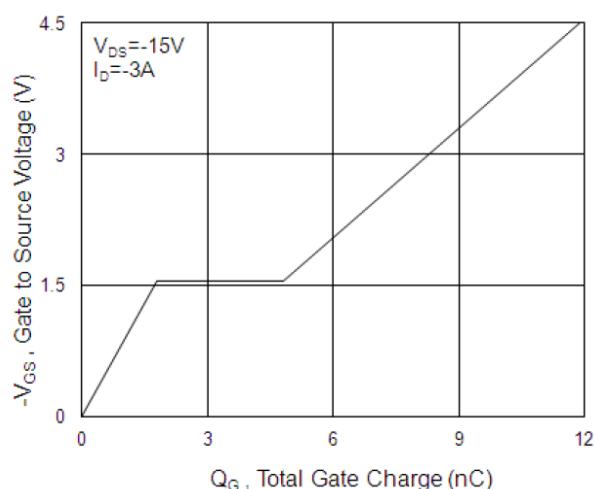


Fig.4 Gate-Charge Characteristics

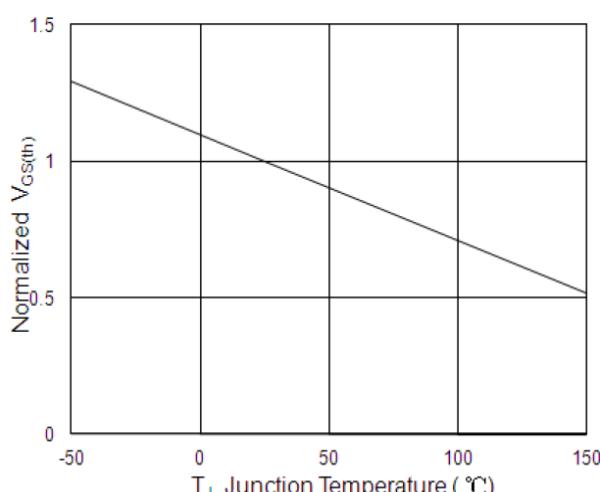


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

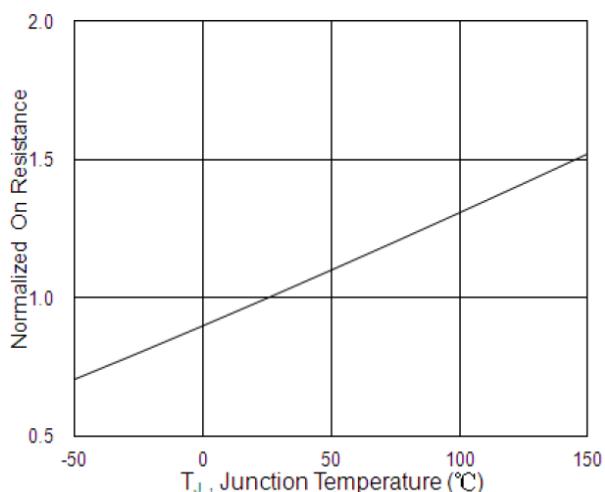


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

CHARACTERISTIC CURVES

