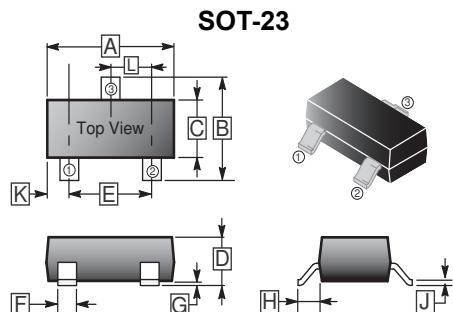


RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

The SMS3400Y-C provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness. The SOT-23 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



FEATURES

- Lower Gate Charge
- Simple Drive Requirement
- Fast Switching Characteristic

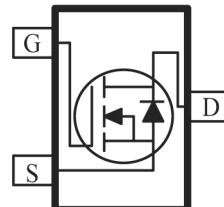
MARKING

3400.

REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.70	3.10	G	0	0.18
B	2.10	3.00	H	0.55	REF.
C	1.20	1.80	J	0.08	0.26
D	0.89	1.3	K	0.6	REF.
E	1.70	2.3	L	0.95	BSC.
F	0.30	0.50			

PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-23	3K	7 inch



ORDER INFORMATION

Part Number	Type
SMS3400Y-C	Lead (Pb)-free and Halogen-free

ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current @Steady State	I_D	5.6	A
		4.5	
Pulsed Drain Current ¹	I_{DM}	23	A
Maximum Power Dissipation	$T_A=25^\circ\text{C}$	P_D	1.2 W
Thermal Resistance Junction-Ambient ² @Steady State	$R_{\theta JA}$	104	$^\circ\text{C} / \text{W}$
Operating Junction & Storage Temperature	T_J, T_{STG}	150, -55~150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	30	-	-	V	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$
Gate-Threshold Voltage	$V_{GS(\text{th})}$	0.65	-	1.5	V	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 12\text{V}$, $V_{DS}=0\text{V}$
Drain-Source Leakage Current	I_{DSS}	-	-	1	μA	$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	-	21	27	mΩ	$V_{GS}=10\text{V}$, $I_D=5.6\text{A}$
		-	25	33		$V_{GS}=4.5\text{V}$, $I_D=5\text{A}$
		-	33	51		$V_{GS}=2.5\text{V}$, $I_D=3\text{A}$
Total Gate Charge	Q_g	-	4.8	-	nC	$V_{GS}=4.5\text{V}$
Gate Source Charge	Q_{gs}	-	1.2	-		$V_{DS}=15\text{V}$
Gate Drain Charge	Q_{gd}	-	1.7	-		$I_D=5.6\text{A}$
Turn-on Delay Time	$T_{d(\text{on})}$	-	12	-	nS	$V_{GS}=4.5\text{V}$
Rise Time	T_r	-	52	-		$V_{DD}=15\text{V}$
Turn-off Delay Time	$T_{d(\text{off})}$	-	17	-		$R_{\text{GEN}}=2.8\Omega$
Fall Time	T_f	-	10	-		$I_D=1\text{A}$
Input Capacitance	C_{iss}	-	535	-	pF	$V_{GS}=0\text{V}$
Output Capacitance	C_{oss}	-	130	-		$V_{DS}=15\text{V}$
Reverse Transfer Capacitance	C_{rss}	-	36	-		f = 1MHz
Source-Drain Diode						
Forward Voltage	V_{SD}	-	-	1.2	V	$V_{GS}=0\text{V}$, $I_S=5.6\text{A}$

Notes:

1. Pulse Width≤300μs, Duty Cycle ≤ 2%.
2. Surface Mounted on FR4 Board, When Mounted on 1 inch² FR4 Board.

CHARACTERISTIC CURVES

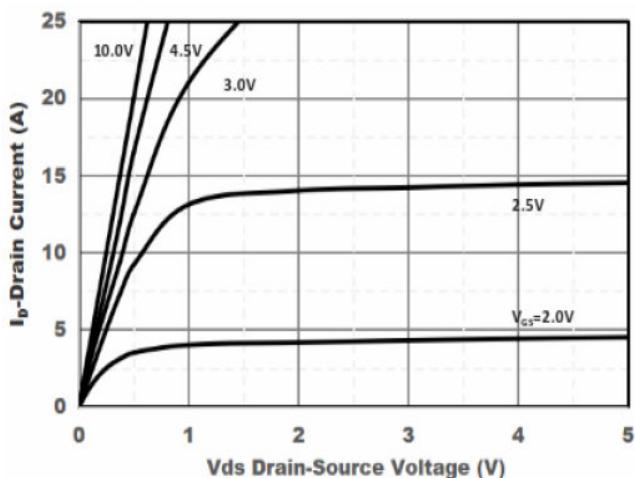


Figure1. Output Characteristics

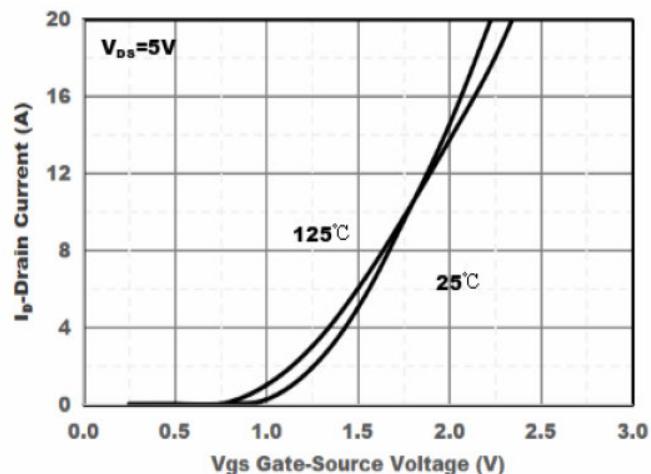


Figure2. Transfer Characteristics

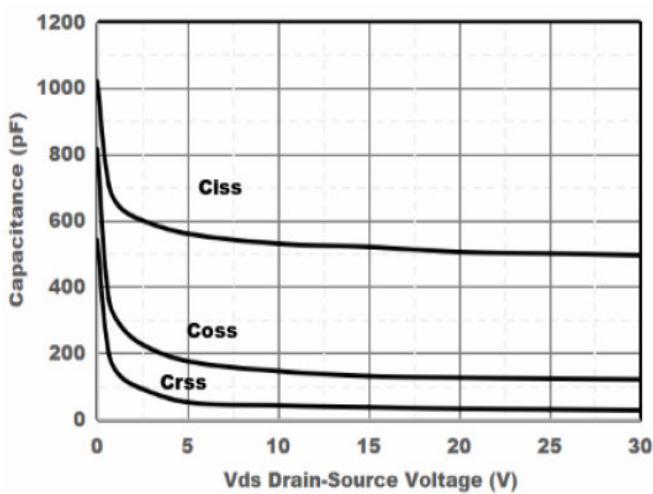


Figure3. Capacitance Characteristics

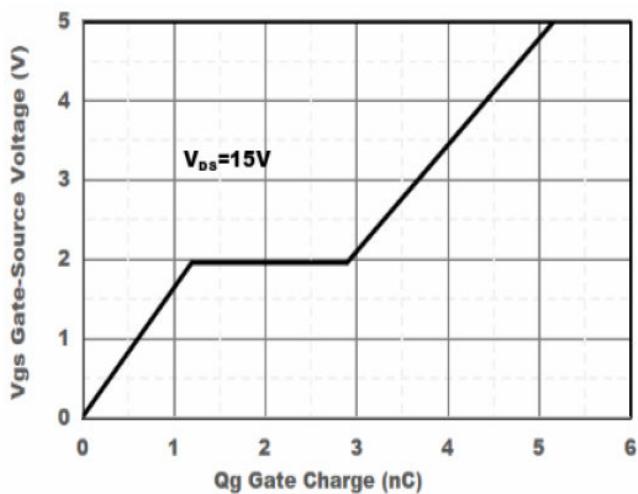


Figure4. Gate Charge

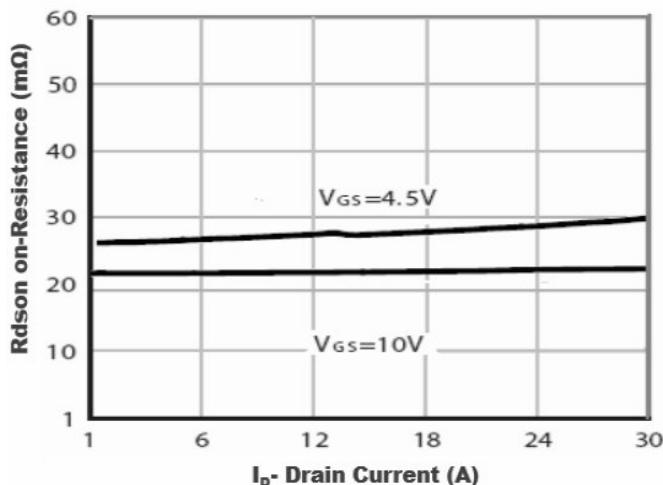


Figure5. Drain-Source on Resistance

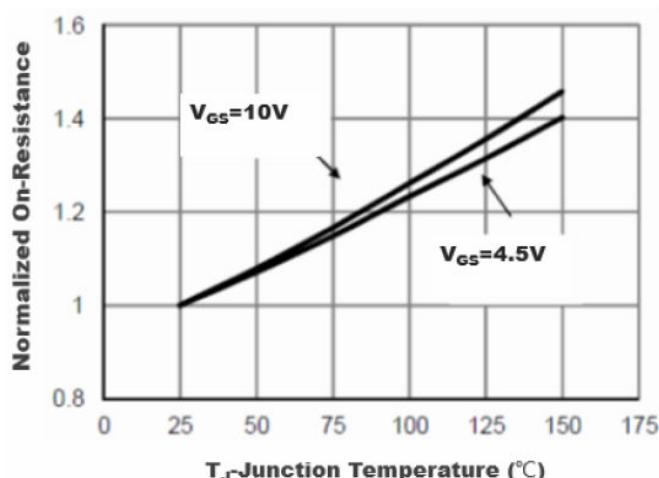


Figure6. Drain-Source on Resistance

CHARACTERISTIC CURVES

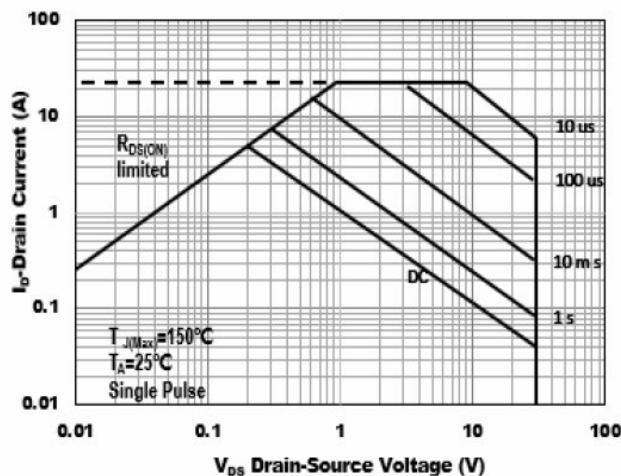


Figure 7. Safe Operation Area

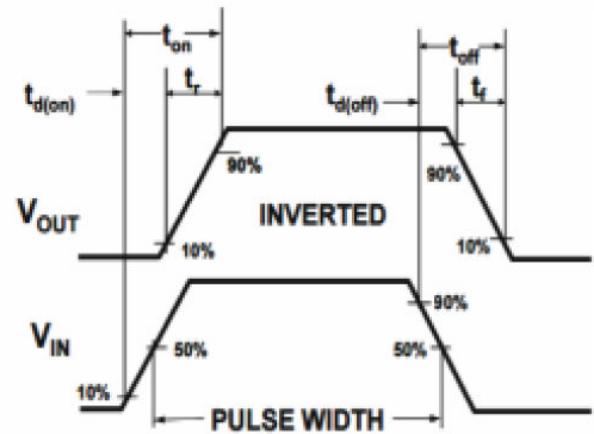


Figure 8. Switching wave